



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Haruo Hyodo et al.  
Serial No. : 09/963,839  
Filed : September 26, 2001

Art Unit : 2818  
Examiner : Quoc Hoang  
Confirmation No.: 9344  
Notice of Allowance Date: April 27, 2004

Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEROF

**MAIL STOP ISSUE FEE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

COMMENTS ON REASONS FOR ALLOWANCE

The Examiner's allowance of claims 1, 3-5 and 7 is appreciated. The Applicant submits the following comments concerning the Examiner's Reasons for Allowance at page 2 of the Notice of Allowability. It is the subject matter of each claim as a whole that is patentable, not any particular feature recited in the claim(s). The Applicant does not concede that the reasons for allowance given by the Examiner are the only reasons that make the claims allowable and does not make any admission or concession concerning the Examiner's statement on page 2 of the Notice of Allowability.

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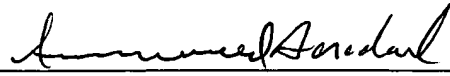
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Applicant : Haruo Hyodo et al.  
Serial No. : 09/963,839  
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Page : 2 of 2

Attorney's Docket No.: 10417-100001 / F51-  
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Respectfully submitted,

Date: 7/22/04

  
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